

APPENDIX A

The following is C language source code to implement transactional optimization based on observable state using new value logging (NVL). (It is also possible to implement this optimization using old value logging or other transactional mechanisms.)

```

5  /*
   * nvl_tran.c
   * transaction logging mechanism
   *
10  * This transaction mirror is initially designed for BYTE and SHORT atomic updates.
   * Because this type of mirror request a logged values scan for each read access,
   * it relies on read access drivers to be implemented.
   *
   * Each time a java method makes more than 1 atomic write (BYTE or SHORT)
15  * the transaction mechanism is advantageous, but it has to be under certain conditions.
   * Writings must occurs between 2 flush points which are:
   * - observable state points, such as communications
   * - System limitations, such as Util.array_xxx methods or certain other native methods
   * (Crypto for ex) that rely on validity of java byte/short/arrays.
20  */

#include <gpos.h>
#include <nvl_tran.h>
25  #if _include_NEW_VALUE_LOGIN_MIRROR == ON

#define SDBG(x)          //x          /* statistical information display */

30  SDBG(static BYTE _SXDATA maxNVLVolatileEntriesCount = 0;)
  SDBG(static WORD _SXDATA doubleAccessEntry = 0;)
  SDBG(static WORD _SXDATA eepCumulAdvantage = 0;)

  /* EEPROM NVL mirror */
35  #ifdef _WIN32
    #define NVLPersistentMirror          SYS_Persistent.NVL_PersistentMirror
  #else
    NVL_MIRROR _SXDATA NVLPersistentMirror;
  #endif

40  #define NVLPersistentCrc          NVLPersistentMirror.crc
  #define NVLPersistentEntriesCount NVLPersistentMirror.count
  #define NVLPersistentEntries          NVLPersistentMirror.entries

45  /* RAM NVL mirror */
  #define NVLVolatileMirror          SYS_VolatileBuffer.NVL_VolatileMirror
  #define NVLVolatileCrc          NVLVolatileMirror.crc
  #define NVLVolatileEntriesCount    NVLVolatileMirror.count
  #define NVLVolatileEntries          NVLVolatileMirror.entries
50

  /*
    NVL_ScanEntries
    parse the volatile Logging mirror and return the entry value,
55    if an entry matches the input address.
  */
  E_ADDRESS NVL_ScanEntries(E_ADDRESS addr)
  {
60    BYTE iEntry;

    for (iEntry = 0; iEntry < NVLVolatileEntriesCount; iEntry++) {
      if (VIRTUAL_ADDRESS(NVLVolatileEntries[iEntry].dest) == addr) {

65          /* an entry matches the address, return the indirect one */
          switch (NVLVolatileEntries[iEntry].type) {

```

```

                                case T_BYTE:
                                    return
(E_ADDRESS)&(NVLVolatileEntries[iEntry].value.bw.low);
                                    break;
5                                case T_SHORT:
                                    return
(E_ADDRESS)&(NVLVolatileEntries[iEntry].value);
                                    break;
10                                }
                                /* we should never arrive here */
                                SC_ASSERT(FALSE);
                                }
                                }
15                                /* no entry matches the address, return the original one */
                                return addr;
                                }

/*
20    NVL_FlushReset
    Check validity of the persistent mirror thanks its crc.
    Flush value entries from the persistent mirror to their destinations.
    Reset persistent and volatile mirror once operation is over.

25    Note: if a tear occurs during reset, invalidity of crc will help us catch it.
    */
void NVL_FlushReset()
{
30    BYTE iEntry;
    WORD wLength;

    if (NVLPersistentEntriesCount > 0) {

        /* it is possible that a tearing occurs during the persistent buffer update,
35        hence NVLPersistentEntriesCount could be a meaningless value. If it is
        the case, the entire buffer is reset */
        if (NVLPersistentEntriesCount > MAX_NLV_ENTRIES) {
            wLength = sizeof(NVL_MIRROR);
        } else {
40            //wLength = offsetof(NVL_MIRROR,entries[NVLPersistentEntriesCount]);

            wLength = sizeof(BYTE) + sizeof(WORD) + (NVLPersistentEntriesCount *
sizeof(NVL_ENTRY));
        }
45        if
(_OS_ComputeCRC16(VIRTUAL_INDEX(&NVLPersistentEntriesCount),(WORD)(wLength -
sizeof(WORD))) == NVLPersistentCrc) {
            for (iEntry = 0; iEntry < NVLPersistentEntriesCount; iEntry++) {
50                switch (NVLPersistentEntries[iEntry].type) {
                    case T_BYTE:

                        _OS_WriteMemory((GEN_ADDRESS)&NVLPersistentEntries[iEntry].value.bw.low,
55                        NVLPersistentEntries[iEntry].dest,(WORD)sizeof(BYTE));
                        break;
                    case T_SHORT:

                        _OS_WriteMemory((GEN_ADDRESS)&NVLPersistentEntries[iEntry].value,
60                        NVLPersistentEntries[iEntry].dest,(WORD)sizeof(WORD));
                        break;
                }
            }
        }

65        /* clear the NVL Volatile buffer (crc + count + n*entries) */
        _OS_ClearRAMBuffer((GEN_ADDRESS)&NVLVolatileMirror, wLength);
        /* reset the NVL Persistent Buffer (crc + count + n*entries) */

```

that "6065001"

```

        _OS_WriteMemory((GEN_ADDRESS)&NVLVolatileMirror,
VIRTUAL_INDEX(&NVLPersistentMirror), wLength);
5    }

/*
    NVL_SaveFlushReset
10    Backup the volatile Logging mirror into its persistent image.
    Call the FlushReset function once operation is over.
*/
void NVL_SaveFlushReset()
{
15    WORD wLength;

    SDBG(
        fprintf(stderr, "flush NVL mirror : number of entries = %x\n",NVLVolatileEntriesCount);
        if (NVLVolatileEntriesCount > maxNVLVolatileEntriesCount)
20    maxNVLVolatileEntriesCount = NVLVolatileEntriesCount;
        fprintf(stderr, "Current Max number of entries = %x\n",maxNVLVolatileEntriesCount);
        fprintf(stderr, "number of already present entry NVL access =
        %x\n",doubleAccessEntry);

25    if (NVLVolatileEntriesCount > 0) {
        eepCumulAdvantage += ((NVLVolatileEntriesCount * 3) - (1 /* saving to EEPROM
buff */ + 1 /* resetting EEPROM buff */ + NVLVolatileEntriesCount /* number of flushes */));
    }
    fprintf(stderr, "number EEPROM writing saved thanks NVL =
30    %x\n",eepCumulAdvantage);
    fprintf(stderr, "total number EEPROM writing saved = %x\n",eepCumulAdvantage +
    doubleAccessEntry);
}

35    if (NVLVolatileEntriesCount > 0) {
        /* working length */
        //wLength = offsetof(NVL_MIRROR,entries[NVLVolatileEntriesCount]);
        wLength = sizeof(BYTE) + sizeof(WORD) + (NVLVolatileEntriesCount *
40    sizeof(NVL_ENTRY));
        /* compute crc */
        NVLVolatileCrc =
        _OS_ComputeCRC16(VIRTUAL_INDEX(&NVLVolatileEntriesCount),(WORD)(wLength -
        sizeof(WORD)));
45    /* backup to the persistent image */

        _OS_WriteMemory((GEN_ADDRESS)&NVLVolatileMirror,VIRTUAL_INDEX(&NVLPersistentMi
rror),wLength);
        /* flush the persistent mirror & reset both persistent and volatile buffers */
50    NVL_FlushReset();
    }
}

55 /*
    NVL_LogEntry
    Add an entry in the Logging volatile mirror, if entry is not already existing.
    If volatile mirror is getting full: backup,flush and reset it.
*/
void NVL_LogEntry(INDEX dest, BYTE type, WORD value)
{
60    BYTE iEntry;

    /* check if an entry is already present is the Volatile Buffer */
65    for (iEntry = 0; iEntry < NVLVolatileEntriesCount; iEntry++) {
        if (NVLVolatileEntries[iEntry].dest == dest) {
            SDBG(doubleAccessEntry++;)
        }
    }
}

```

Case 1:35-cv-00001-1

```

        break;
    }
}

5    NVLVolatileEntries[iEntry].dest = dest;
    NVLVolatileEntries[iEntry].type = type;
    NVLVolatileEntries[iEntry].value.w = value;

    if (NVLVolatileEntriesCount == iEntry) {
10        NVLVolatileEntriesCount++;
        /* check if there is enough place for another entry, if not save and flush */
        if (NVLVolatileEntriesCount == MAX_NLV_ENTRIES) {
            NVL_SaveFlushReset();
        }
15    }
}

20 /*
 * _VM_WriteByteSecu()
 * Writes a byte in Eeprom at address specified
 */
25 void _VM_WriteByteSecu(INDEX iaddress,BYTE bvalue)
{
    DBG(fprintf(stderr, "Write BYTE through NVL mirror\n");)

30    if (IS_SETFLAG2(OS_TransactionActive)) {
        _OS_WriteByteSecu(iaddress, bvalue);
    } else {
        if (IS_EEPROM(iaddress)) {
            NVL_LogEntry(iaddress,T_BYTE,(WORD)bvalue);
35        } else {
            VIRTUAL_ADDRESS(iaddress)[0] = bvalue;
        }
    }
}

40 /*
 * _VM_WriteWordSecu()
 * Writes a word in Eeprom at address specified
 */
45 void _VM_WriteWordSecu(INDEX iaddress,WORD wvalue)
{
    DBG(fprintf(stderr, "Write WORD through NVL mirror\n");)

50    if (IS_SETFLAG2(OS_TransactionActive)) {
        _OS_WriteWordSecu(iaddress, wvalue);
    } else {
        if (IS_EEPROM(iaddress)) {
55            NVL_LogEntry(iaddress,T_SHORT,wvalue);
        } else {
            (*(WORD*)(VIRTUAL_ADDRESS(iaddress))) = (WORD)wvalue;
        }
    }
60 }
}

#endif

```

Case 5:05-cv-00555-001